



IFW

EXAMINER: Joseph Chang
GROUP: 2817
APPLICANT: Gerald Harron et al
SERIAL NO: 10/796,418
FILED: March 10, 2004
FOR: MODULATION USING DISCRETE AMPLITUDE ADJUSTMENT
AND DUAL DIGITAL DELAY LINES

U.S. Patent and Trademark Office
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Randolph Building
401 Dulany Street
Alexandria, VA 22314
U.S.A.

Attention: John Fitzburgh

Dear Sir:

Responsive to the Notice Regarding Drawings dated November 18, 2005,
please find enclosed herewith the replacement drawings (4 sheets) as requested. Also
enclosed herewith are copies of the drawings (4 sheets) as originally filed.

Respectfully submitted

GERALD HARRON ET AL

PER: MRW/II
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December 22, 2005
Enc.(9)
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